

Figure 1

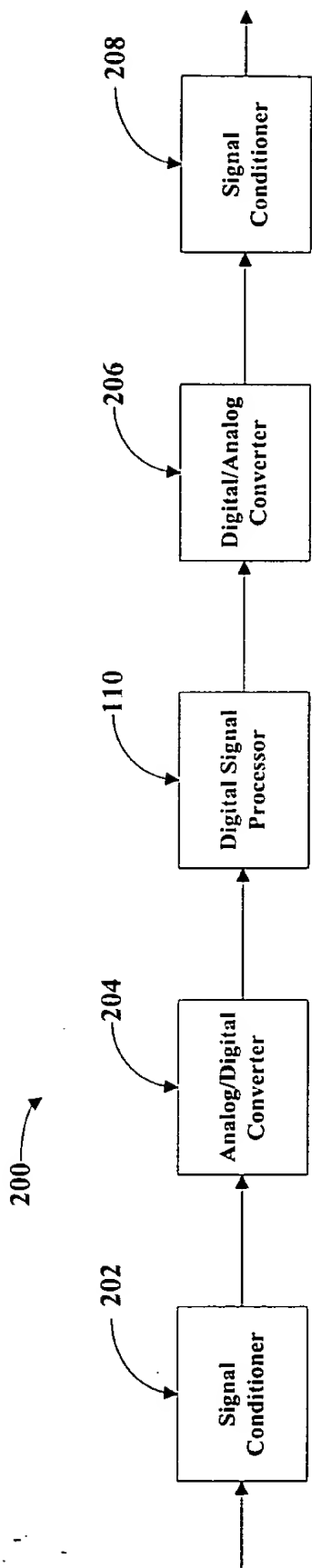


Figure 2

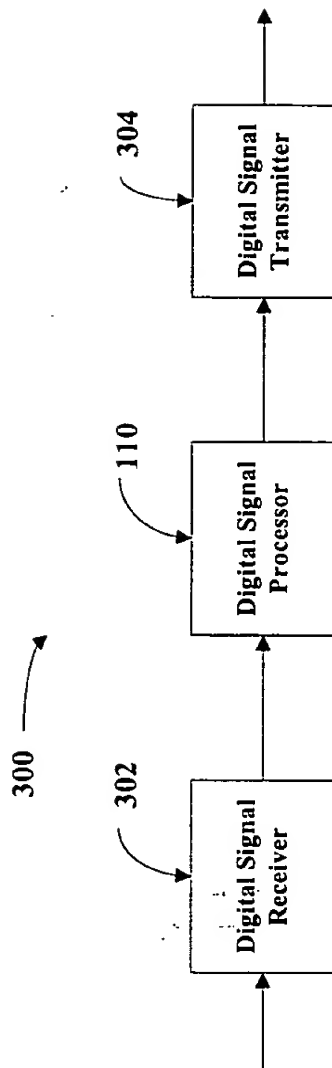


Figure 3

400

|     | IF1     | IF2 | DEC | AC  | EX1 | EX2 | EX3 | WB |
|-----|---------|-----|-----|-----|-----|-----|-----|----|
| 1   | i       |     |     |     |     |     |     |    |
| 2   | i+1     | i   |     |     |     |     |     |    |
| 3   | i+2     | i+1 | i   |     |     |     |     |    |
| ... | ...     | i+2 | i+1 | ... |     |     |     |    |
| ... | ...     | ... | i+2 | ... | ... |     |     |    |
| n   | i+(n-1) | ... | ... | ... | ... | ... |     |    |

Clock  
Cycle

Figure 4

1507

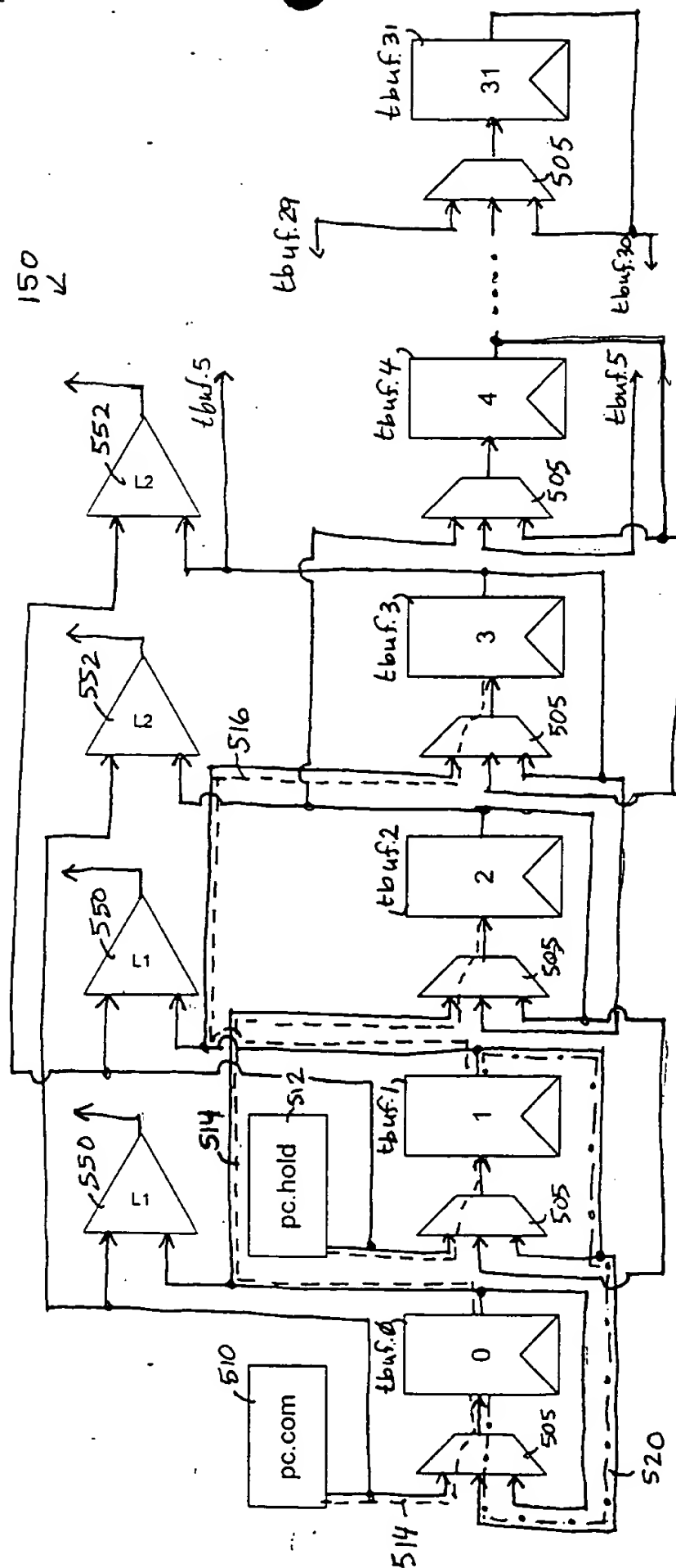


Fig. 5

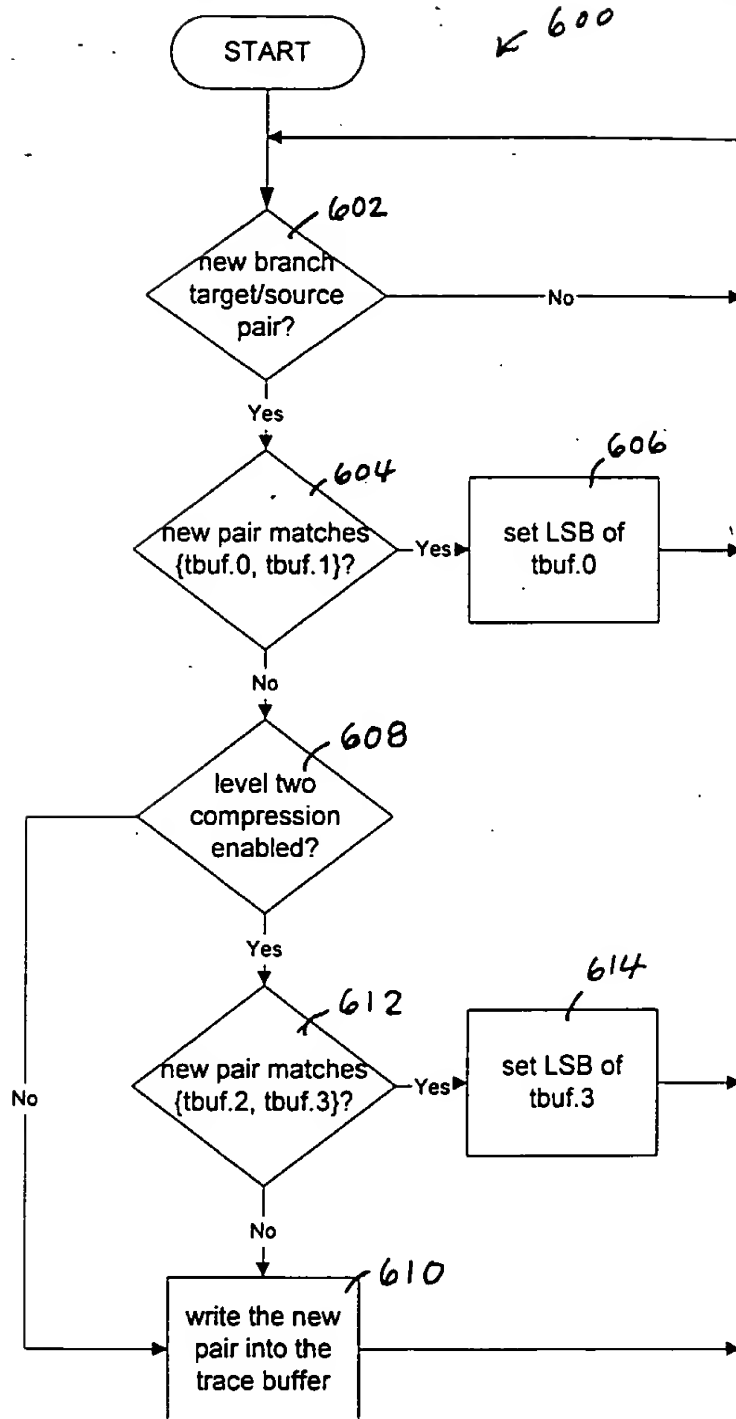


FIG. 6

700

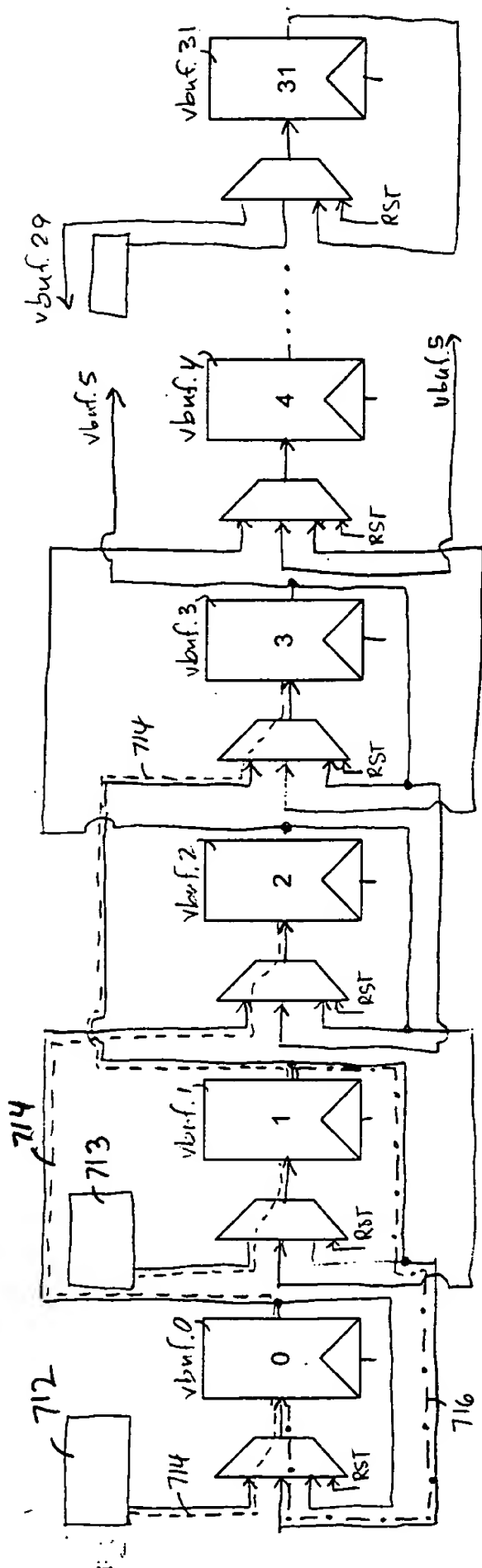


FIG. 7